REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE EXAMINING GROUP 2192

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/618,829 Filed: July 14, 2003 Inventor(s): Meyer et al.	 Examiner: Romano, John J. Group/Art Unit: 2192 Atty. Dkt. No: 5681-41100 Conf. No. 5944 	
Title: Method and System for Expressing the Algorithms for the Manipulation of Hardware State Using an Abstract Language	S ANEIL S S ATO 10/23/01	3

AMENDMENT; RESPONSE TO FINAL OFFICE ACTION OF July 23, 2007

MAIL STOP AF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 23, 2007, to further highlight why the application is in condition for allowance.